

10066833 7/29/04

| L Number | Hits | Search Text | DB | Time stamp |
|----------|---------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|------------------|
| 1 | 224 | (712/219).CCLS. | USPAT; US-PGPUB | 2004/07/29 15:21 |
| 10 | 1 | ("5867701").PN. | USPAT; US-PGPUB | 2004/07/29 16:11 |
| 19 | 1 | high-performance and multi-processor and floating and point and unit | EPO | 2004/07/29 16:23 |
| - | 1 | ("20020116601").PN. | USPAT; US-PGPUB | 2004/07/28 09:59 |
| - | 1 | ("20020152368").PN. | USPAT; US-PGPUB | 2004/07/29 09:00 |
| - | 568652 | pipeline processor microprocessor cpu | USPAT; US-PGPUB | 2004/07/29 09:01 |
| - | 751179 | stack status flush "return address" push pop "save state" | USPAT; US-PGPUB | 2004/07/29 09:29 |
| - | 2264 | "instruction bundle" vliw "very long instruction word" "instruction package" | USPAT; US-PGPUB | 2004/07/29 09:04 |
| - | 1323490 | hard\$1code\$ embedded cache memory stored ram rom | USPAT; US-PGPUB | 2004/07/29 09:09 |
| - | 10001 | ((interrupt function exception disrruption instruction) near call) | USPAT; US-PGPUB | 2004/07/29 09:10 |
| - | 33273 | ((code state context function) near switch) | USPAT; US-PGPUB | 2004/07/29 09:11 |
| - | 195881 | (pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state") | USPAT; US-PGPUB | 2004/07/29 09:12 |
| - | 14126 | (pipeline processor microprocessor cpu) and (((code state context function) near switch)) | USPAT; US-PGPUB | 2004/07/29 09:12 |
| - | 8310 | (pipeline processor microprocessor cpu) and (((interrupt function exception disrruption instruction) near call)) | USPAT; US-PGPUB | 2004/07/29 09:12 |
| - | 7844 | ((pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")) and ((pipeline processor microprocessor cpu) and (((code state context function) near switch))) | USPAT; US-PGPUB | 2004/07/29 09:13 |
| - | 557 | ((((pipeline processor microprocessor cpu) and (stack status flush "return address" push pop "save state")) and ((pipeline processor microprocessor cpu) and (((code state context function) near switch)))) and ((pipeline processor microprocessor cpu) and (((interrupt function exception disrruption instruction) near call))) | USPAT; US-PGPUB | 2004/07/29 09:15 |
| - | 660 | ((((interrupt function exception disrruption instruction) near call)) and (((code state context function) near switch))) | USPAT; US-PGPUB | 2004/07/29 09:17 |
| - | 11 | ((((interrupt function exception disrruption instruction) near call)) near (((code state context function) near switch))) | USPAT; US-PGPUB | 2004/07/29 09:15 |
| - | 6925 | ((((interrupt function exception disrruption instruction) near call)) and handl\$ | USPAT; US-PGPUB | 2004/07/29 09:18 |
| - | 145 | ((((interrupt function exception disrruption instruction) near call)) near handl\$ | USPAT; US-PGPUB | 2004/07/29 09:18 |
| - | 8385 | "stack status" (pipeline near flush) "return address" "save state" "return state" | USPAT; US-PGPUB | 2004/07/29 09:29 |
| - | 5853 | ("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu) | USPAT; US-PGPUB | 2004/07/29 09:30 |
| - | 1223 | ((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call)) | USPAT; US-PGPUB | 2004/07/29 09:35 |

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|---|------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|--------------------|------------------|
| - | 4055 | ((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and handl\$ | USPAT; US-PGPUB | 2004/07/29 09:35 |
| - | 1018 | ((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$ | USPAT; US-PGPUB | 2004/07/29 09:45 |
| - | 478 | ((("stack status" (pipeline near flush) "return address" "save state" "return state") and (pipeline processor microprocessor cpu)) and (((interrupt function exception disrruption instruction) near call))) and handl\$) and pipeline | USPAT; US-PGPUB | 2004/07/29 09:48 |
| - | 478 | ((((interrupt function exception disrruption instruction) near call)) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and handl\$ and pipeline | USPAT; US-PGPUB | 2004/07/29 09:50 |
| - | 5 | (((((interrupt function exception disrruption instruction) near call)) near handl\$) and ("stack status" (pipeline near flush) "return address" "save state" "return state") and pipeline | USPAT; US-PGPUB | 2004/07/29 09:50 |

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